

A Diverse View on Feature-Oriented Programming in Software Product Line

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Abstract—The software product line (SPL) is a procedure of increasing products' set, in which variability is essential phenomenon taken into variable model. Fields & variables of SPL have been subject to wide research over previous years. Feature-oriented programming (FOP) is programming method for implementing SPLs created on composition appliances known as refinements. In this paper, we have described FOP with software product line engineering (SPLE). Various researchers have worked on FOP. Different types of approaches for software product line also defined.

Keywords—Software Product Line, Software Product Line Engineering, Feature-oriented programming, Refactoring.

INTRODUCTION

SPL is the development of a set of engineering products that may be reused using general architecture & a predetermined plan. SPL's products may be embedded systems, software products, or software systems of some kind, such as digital services. SPL has become an important and popular way to improve quality, reuse support & efficiently achieve different product types. Variability management over years of research & training to become a central concern associated with SPLs [1].

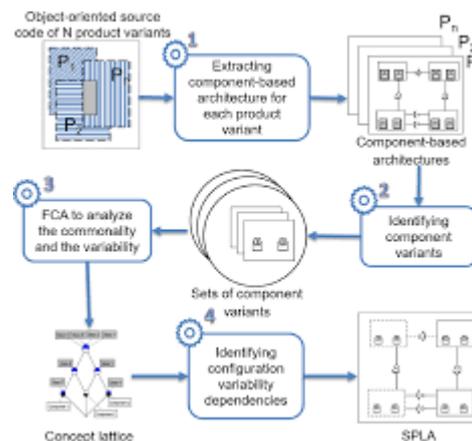


Fig.1. Software product line Architecture

SPL Development is a model towards generating “a set of software-intensive systems sharing a common, managed set of features that satisfy the specific needs of a particular market segment or mission and that are developed from a common set of core assets in a prescribed way” [2]. Based on recyclable platform, SPL Development enables a large number of products to be customized to suit the needs of individual

consumers with minimal development time & superior product quality [3]. Software discovery clarifies clear search links between all handicrafts in the software development process & applies them towards the software development process (SDP). [4]. Although standardization has enabled the software industry to be significantly capable of software progress & to deliver affordable software for a wider market, it often does not address the needs and desires of small market segments or individual customers. SPLs combine standardization & mass production by mass customization into software Engg. Software manufacturers may create software products created on customer's needs founded on set of reusable parts. Perception of aspects is critical towards accomplishing this automation's level, as it minimizes differences among customer needs & functionality product delivers. Aspects are central concept at every stage of product-line development. Writers proceed approach of developer & specifically focus on improvement, maintenance, & execution of product line variables founded on user's feature selection. [5].

LITERATURE SURVEY

Inmaculada Ayala et al. [2019] Integrate targeted and SPL approaches to develop & guide the development of multi-agent structures into the context of cyber-physical structures. We describe models' set (iStar 2.0 via target, CVL model via variable) & spontaneously work with agents on multiple heterogeneous devices, each by different configuration, defined by an algorithm for SPL process & development Propose process. We refer to this suggestion in context of domestic energy management system. Lastly, we verified scalability and presentation of proposal by casually constructed model. Consequences suggest that large iStar models of 10000 components may be handled in seconds with our approach [6].

Andras Kicsi et al. [2019] Suggest automated approaches to remove feature-to-program associates, with high-level aspects delivered through domain experts. We associate call data by text comparison among code & high-level attributes for this determination. In addition, the discovery of communities between programs and feature engagements supports a deeper understanding of feature specificity. Because attributes originate by domain experts, community analysis exposes differences among specialist's approach & interior code structure. We originate that groups with above half feature codes into specific communities fit well with high-level attributes. We report on two levels of testing and over 2,000 Magic 4GL programs into an industrial SPL adoption project [7].

Shatnawi et al. [2017] suggest a method towards the rear engineer architecture of product's set versions. Our purpose is to recognizing variables & dependencies between architectural elements alternatives. To analyze variability in our work relies on theoretical concept analysis. To confirm suggested method, we estimated 2 families of open-source product variants; Health Monitor and Mobile Media. Consequences of retrieved architectural variability & reliability accuracy and recall measurements were 81%, 91%, 67% and 100%, respectively. [8].

W. Fenske et al. [2017] suggest step-wise, semi-automated procedure towards transfer cloned product variants towards facilitating SPL. Our procedure trusts on clone detection towards recognizing general codes for several variants & new, & variant-protecting refactoring towards removing general codes. We estimated

our method on 5 cloned product variants & reduced code clone through 25%. In addition, we deliver qualitative statistics on possible limits & probabilities to remove more redundant code. We say that our method may efficiently reduce synergistic determination related to clone-and-development & decrease long-term costs of preservation & development. [9].

J. Ghofrani et al. [2017] use consequences of certain convolutional neural networks (CNNs) via code summation towards detect code clones. We utilize created description via 2 code snippets like metric to measure similarity among them. We suggest a vector comparison measure via calculating the comparison index among these dimensions, which can determine which code snippets are cloned. [10].

R. V. Patil et al. [2015] Shows importance of finding equality. The similarity is found by operator or function overloading. Because it is an indispensable feature of noble object-oriented language. It discourses main methods that save time into retrieving & comparing data by extracting & configuring mining code by code document. The proposed system eliminates attempts to line-up code lines among 2 files following traditional algo. It describes reduction method & code complexity based analysis, increasing likelihood of success. The conclusion is that no single scheme describes procedures by detecting all types of clones. We existing multi-modal learning techniques to finding different kinds of code clones, which are occupied as problem statements into this task. [11].

H. Eyal-Salman et al. [2013] Suggests new method towards increase performance of IR approaches while useful towards software variants' group. The originality of our method is two-fold. On one hand, this uses what software variants (SV) have into general & how they vary towards increase accuracy of IR consequences. Conversely, towards increase no. of relevant recovered links, it decreases abstraction gap among aspects & source code with presenting an intermediary level known as code-subject right of left. We applied our method towards gathering 7 variants of the large-scale systems with ArgoUML-SPL modeling device. Trial outcomes presented that our method transfers traditional application of IR approaches & latest & important work on topic [12].

A. Hemel and R. Koschke [2012] To explain this venture, CEWG examined us towards finding out how much up-and-upstream code may be originated into industrial products, & to what extent and to what fragment of kernel. We utilized clone detection methods extensively to relate several Linux versions with their vendor-specific alternatives. We originate several variations that were not ported back. Certain changes have also been found in Linux subsystem, where neither we nor Linux Foundation suppose this. We originate improvements in conventional kernels that were not combined into vendor code. Complete, our examination delivers sufficient indication towards support requirement via an LTSI & well cooperation between Linux developers in mainstream and vendor variants. [13].

Y. Jun et al. [2011] Suggest the use of an FOP method to differentiate CC into system / TLM (Transaction Level Model) modeling & to associate it with the object-oriented approach. Evaluation displays benefits of our facility-based approach. The problem is important principle of SE. Given that TLM/ SystemC models are actually software programs that mimic nature of hardware, this principle ought to be surveyed into TLM/ SystemC modeling & has an application in Separation of Communication & Computation (CC Separation). But, most of the remaining CSC/ TLM models do not differentiate into CCs, so changing their computation models or communication protocols leads to unnecessary development efforts. [14].

J. Ye et al. [2010] Refactor SoCLIB recommends using FOP method that reusable above problem and solve performance of each TLM-DT model. Though this paper may seem particular to SoCLib, concept of transaction-level modeling by FOP method is common. The transaction level is not an abstract level. Giving towards OSCI TLM-2.0 Language Reference Manual, this is more distributed in Approximate Timeout (LT), Loaded Timed (AT), & Untimed (UT). Various use cases provide different sub-level service. But for each component, SoCLib delivers solitary one kind of TLM, that is, TLM-DT. DT means the delivery time, & TLM-DT model may be compared by LT model. Therefore this is major task to discover good way to improve two additional kinds of TLMs via every module. This is unwise towards improving them by scratch as they share similar functionality by the TLM-DTT model and can be reused in their implementation [15].

THE REFERENCE SOFTWARE PRODUCT LINE TESTING PROCESS

SPL test has a W-shaped life cycle [16] called extended V-model, which is composed of two overlapping V-models, as shown in Fig. 2. The arrows pointing by left to right into Fig. 2 show that domain test property is utilized as input towards application test. Test properties, for example test scenarios, test plans, & test cases, should be built into respective engineering stages. To identify some test properties, test engineers initiate system testing into application or domain necessities engg. stage, application architecture design or domain integration testing, application identification or unit testing into the domain.

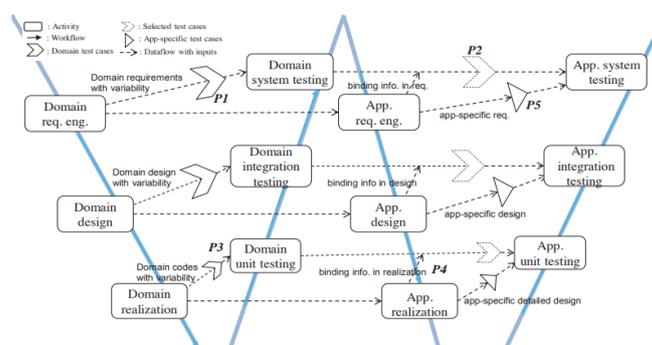


Fig. 2.A reference SPL testing process

Test properties may be created & implemented through domain inspection or application verification. But, it is appropriate towards examining main property in domain testing therefore to application testing

may concentrate on application-specific fragments that are not included in domain testing. Under normal circumstances, domain engineering is not focused on core asset development because it does not achieve the full range of products during domain engineering. So, in maximum cases, domain system testing may simply be shown in restricted mode [17].

In this view, SPL validation process in Figure 2 shows 2 kinds of test cases for reference SPL testing, application test cases (test cases created in application Engg.) and domain test cases (test cases built-in domain Engg.). Domain test cases must contain methods that may be effectively recycled into application testing & address variables. In accumulation, test information should be determined via normality & variability for test cases [18].

I. CURRENT SOFTWARE PRODUCT LINE APPROACHES

In this segment, several existing SPL methods are provided as a synopsis. These methods are [19]:

A. FAST (Family-oriented abstraction, specification, and translation)

This is a feature-based model proposed by Weiss et.al [20]. It helps in applying product-line principles to software engineering process. It can be used in cases where a range of products are developed which have major share of common artifacts among themselves. These common features can be common behavior, common interfaces, or common code.

B. FODA

FODA [21] (Feature-Oriented Domain Analysis) has been proposed through Kang to identify & model features. It is based on a domain analysis technique in which distinct features within a product line are identified. These features combined together to define the domain of the product family. This approach is followed because variability purpose mechanisms that are specified in modules are produced with intends of domain-specific language which is data-intensive allowance of textual version of feature diagrams.

C. FORM (Feature-Oriented Reuse Method for product line software engineering)

FORM (feature-oriented reuse method) is a feature-oriented technique of evaluating domain aspects [22] and then using these features to provide software feature line architecture. In other words, "form" is a systematic way of capturing the changes of common features & applications into domain & focusing on "features" to utilize analytical outcomes to improve domain architecture & modules. FORM method is useful for relating domain analysis outcomes to refillable & adjustable domain modules. There are definite guidelines that work for this.

D. RSEB

RSEB (Rise-Drive Software Engineering Business) [23] is a use-case driven reuse procedure created on UML notation. It is a repetitive, use-case-focused method that facilitates development & reuse of reusable object-oriented software. The main focus of this process is on use cases. Under this process, we first describe requirements of product line domain by support to utilizing cases. Before, domain architecture and reusable artifacts are designed. Lastly, object models are created with the help of this architecture and artifacts which are mapped to the use cases [24].

E. FeatRSEB

FeatRSEB (Reusable Software Engineering Business) is introduced by bringing together FODA and RSEB methods. Two more processes from Foda namely Domain Engineering and Feature Modeling are used to initiate RSEB process. RSEB handles variables methodically in use cases, but no feature model was made in the procedure.

F. ConIPF

Its full form is Formation of Industrial Product Families & it is the European FP6 project [25]. This concept was put forward by Eriksson in whose words ConIPF is “a project which wants to integrate both the product line approach and the structure-oriented configuration 35 technologies”.

G. PuLSE

Fraunhofer Institute Experimental Software Engineering (IESE) designed Product Line Software Engineering (PuLSE) in the late 1990s [26]. According to PuLSE method focus of SPL ought to be on products before on organizational features. PuLSE is composed of 3 main components viz. distribution stage, technical modules & support modules in that order.

H. Kobra

Kobra (Komponentbasierte Anwendungsentwicklung) technique has been established through Fraunhofer Institute Experimental Software Engineering (IESE). This is component-based SPL approach [27]. It is new approach in the fact that it is a grouping of reuse into small concepts into component-based methods & recycles into big concepts in SPL approach.

FEATURE-ORIENTED PROGRAMMING

Software variability [28] refers to combination of aspects that make up a product, & maybe denoted through feature model. Feature model [29] is formal paradigm for capturing and representing general aspects & variations among products that make up a product. Figure 3 shows partial feature model of SPL Tankwise [30]. Certain aspects are precise to several platforms, for example, Handy & PC in this figure. Additionally, there are optional aspects for example map feature size (M_240, M_600, M_780 features).

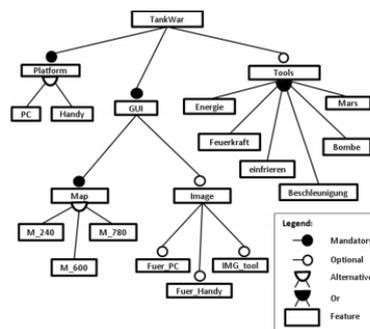


Fig.3. Partial Feature Model of TankWar

Towards improve SPL, we may utilize various methods, such as explanatory & creative. Compositional methods, for example FOP, are implemented as features (physically distinct) code units. FOP is created on stepwise modifications. Stepwise modification is a model for developing complex programs to enhance detail from simple to simple [31]. Principal fractions & program increments are known as revisions & constants, correspondingly. Classes perform basic functions of system (constants) & these functions include attributes (modifications) in extensions.

FOP must not be restricted to the synthesis of source code. There are different other non-code artifacts (for example UML models, grammars, makefiles) that may be specified equational demonstrations, & be derived & made as source code [32].

A. An Example of FOP

In its segment we present feature-based programming founded on (partially) modeling stack by succeeding aspects: [33]:

- **Stack:** provided that pop and push operations at stack.
- **Counter:** enhances local counter (utilized via stack's size)
- **Lock:** accumulation switch towards disallowing or allow alterations of an element (now utilized via stack)
- **Bound:** which applies range check, utilized via stack items
- **Undo:** accumulation an undo function, which reestablishes state like this was formerly last access towards the element

CONCLUSION

SPL testing has 2 distinct but close test engineering functions. Over the past decade, several SPL testing approaches have been developed, & surveys have been conducted on them. Therefore, this paper identifies recent SPL testing approach with describing reference SPL testing process & what is important in SPL testing. SPL is used to develop families of similar software systems in the industry. The restructuring may too be done with specific refactoring towards increase internal quality of existing SPLs. FOP is a designed procedure & tool via program synthesis. The goal is to clarify the features that the target program has to offer and integrate an efficient program that complements these features. FOP has been utilized towards improving product lines into wide variety of domains, comprising compilers via expandable Java languages, fire support simulators via US military, high-performance NW protocols, & program verification tools.

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